Design and Verification of Parallel Programs*

Wang Jian, Chi Xuebin
Supercomputing Center, Computer Network Information Center, Chinese Academy of Sciences,
Beijing 100080, P.R. China,
Email: {jwang,chi}@sccas.cn   **Tel:** +86-10-62659703

ABSTRACT

Due to the uncertainty and parallelism, the design and verification of parallel programs are far more difficult than the serial ones. This paper introduces them for parallel programs as well as several key problems, modes and verification methods applied in parallel programming.

Keywords: parallel program, verification, design, correctness, specification