Researches on Parallel Intrusion Detection Methods
Based-on Network Processor

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ABSTRACT

According to the request of network development and the characteristic of Network Processor, a new network parallel intrusion detection model based on IXP1200 is presented. The parallel process efficiency is improved greatly by using a new method for load distribution based on Micro-Engine. Also, in order to generate more efficient Micro-C code, we suggest a new software architecture that reduces the time of operating memory and checking string.

Keywords: Network Processor, Intrusion Detection, parallel, char string checking